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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
09/526,955	03/16/2000	Meral Bradley Woodberry	SMT 391 ,	1607
759	90 08.07.2002			
	a Patent Counsel	EXAMIN ER .		
	ries of America Inc ic Rim Boulevard	NGUYEN, VINH P		
Camas, WA 98	3607		ART UNIT PAPE	
			2829	
			DATE MAILED: 08/07/2002	

DATE MAILED: 08/07/2002

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)					
	09/526,955	WOODBERRY, MERAL BRADLEY					
Office Action Summary	Examiner	Art Unit					
	VINH P NGUYEN	2829					
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply							
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply - If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, - Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b). Status	16(a). In no event, however, may a reply be time within the statutory minimum of thirty (30) days fill apply and will expire SIX (6) MONTHS from cause the application to become ABANDONEI	nely filed s will be considered timely. the mailing date of this communication. D (35 U.S.C. § 133).					
1) Responsive to communication(s) filed on 23 J	anuary 2002 .						
2a) This action is FINAL . 2b) Thi	s action is non-final.						
3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.							
Disposition of Claims							
4)⊠ Claim(s) <u>1-17</u> is/are pending in the application							
4a) Of the above claim(s) is/are withdray	vn from consideration.						
5) Claim(s) is/are allowed.							
6)⊠ Claim(s) <u>1-17</u> is/are rejected.	6)⊠ Claim(s) <u>1-17</u> is/are rejected.						
7) Claim(s) is/are objected to.							
8) Claim(s) are subject to restriction and/or Application Papers	r election requirement.						
9) The specification is objected to by the Examine	r.						
10) ☐ The drawing(s) filed on is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.							
Applicant may not request that any objection to the	e drawing(s) be held in abeyance. S	ee 37 CFR 1.85(a).					
11) The proposed drawing correction filed on is: a) approved b) disapproved by the Examiner.							
If approved, corrected drawings are required in rep	ly to this Office action.						
12) The oath or declaration is objected to by the Ex	aminer.						
Priority under 35 U.S.C. §§ 119 and 120							
13) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).							
a) ☐ All b) ☐ Some * c) ☐ None of:							
1. Certified copies of the priority documents	s have been received.						
2. Certified copies of the priority documents	s have been received in Applicati	on No					
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 							
14) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).							
a) The translation of the foreign language provisional application has been received. 15) Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.							
Attachment(s)	. ,						
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449) Paper No(s) 2	5) Notice of Informal	y (PTO-413) Paper No(s) Patent Application (PTO-152)					

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1. The restriction requirement on 11/26/2001 is hereby withdrawn.

- 2. The abstract of the disclosure is objected to because legal phraseology such as "comprise" and "comprises" are used. Correction is required. See MPEP § 608.01(b).
- 3. Claims 1-17 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

In claim 1, it is unclear what "electrical source" represents. Is it shown in any of drawings? In claim 2, it is unclear what "power source" represents. Is it shown in any of drawings? In claim 11, it is unclear what "a ground connection", "an outside power source" represent. Are they shown in any of drawings? In claim 16, it is unclear what "heating coils", "anodized aluminum plate", and "a thermostat" represent. Are they shown in any of drawings?

The dependent claims not specifically address share the same indefiniteness as they depend from rejected base claims.

- 4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 5. Claims 1,4-5,7,8,9,11,13,14 are rejected under 35 U.S.C. 103(a) as being unpatentable over Spaziani et al (Pat # 5,631,571).

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As to claims 1,4-5,7-9,11,13 and 14, Spaziani et al disclose a wafer test apparatus as shown in figure 2 having a wafer (100) with a plurality of integrated circuits formed thereon, each of the integrated circuit having an optically sensitive device (203) electrically connected to a component (204,206) and a probe card with plurality of probes (116) extending through an aperture for electrically contacting the components (206,204). It would have been obvious for one of ordinary skill in the art to recognize that the integrated circuit is connected to an electrical source through the probes (116) and one of these probes has to be connected to a ground. Furthermore, it appears that Spaziani et al also suggest that a fiber bundle as shown in figure 2b is also used in transmitting light beams to the optically sensitive device (203).

6. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Belforte et al (Pat # 6,194,909) disclose constructive module of an electronic telecommunications equipment with an interface towards a testing and diagnosing system.

Kamieniecki et al (Pat # 6,388,455) disclose method and apparatus for simulating a surface photovoltage in a substrate.

Edmond et al (Pat # 5,381,103) disclose system and method for accelerated degradation testing of semiconductor devices.

Corr (pat # 6,385,361) disclose optical/electrical inputs for an integrated circuit.

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7. Any inquiry concerning this communication or earlier communications from the examiner should be directed to VINH P. NGUYEN whose telephone number is (703) 305-4914.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the Group receptionist whose telephone number is (703) 305-4900.

VINH P. NGUYEN

PRIMARY EXAMINER

ART UNIT 2829

08/02/2002